

The following SSCS Annual Subsidy Application has been submitted:

Chapter Name: IEEE SSCS Japan Chapter

Date of Application: 10-13-2015

Chapter Street Address: 4-1-1 Kamikodanaka, Nakahara-ku

City: Kawasaki

State and Zip Code: Kanagawa 211-8588

Country: Japan

Chapter Website: <http://www.ieee-jp.org/japancouncil/chapter/SSC-37/ssc.htm>

Requester: Takeshi Yamamura      Email: gauss1022@gmail.com

Position in chapter: Chair

Chapter Chair: Takeshi Yamamura      Email: yamamura.takesh@jp.fujitsu.com

Chapter Treasurer: Tetsuya Iizuka      Email: iizuka@vdec.u-tokyo.ac.jp

Amount of subsidy request: \$2000      Max: \$2,000/year

Total amount requested last year from SSCS: \$2000

Total chapter budget this year (USD): \$2300

Total chapter budget last year (USD): \$2300

Do you plan to apply for an extra subsidy from SSCS? No

Student Chapter? No

Joint Chapter? No      Amount Requested from non-SSCS Sources: \$0

Growth of chapter over the last few years: Constant

Total number of SSCS members expected in chapter this year: 820

Total number of SSCS members in chapter last year: 820

Number of SSCS members that you plan to nominate this year:

IEEE Senior Member = 1 , IEEE Fellow = 1

Number of SSCS members that you nominated last year for:

IEEE Senior Member = 0 , IEEE Fellow = 0

Do you plan to invite an SSCS Distinguished Lecturer to your chapter? No

Brief description of planned activities using the requested SSCS funds:

We are scheduling the following domestic meetings,

Sponsored by SSCS Japan Chapter

(1) Report of ISSCC 2016

Japanese presenters in ISSCC 2016 are invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 10 presenters are invited.

(2) Report of ASSCC 2016 to be co-sponsored with SSCS Kansai Chapter

Japanese presenters in ASSCC 2016 are invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 10 presenters are invited.

(3) Meeting by Distinguished Lecturers

We invite a couple of Distinguished Lecturers to give presentations to VLSI engineers and researchers.

Co-sponsored by SSCS Japan Chapter and SSCS Kansai Chapter

(4) Technical Group Meeting on Memory Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(5) LSI and System Workshop sponsored by IEICE Integrated Circuits and Devices Technical Group

(6) Technical Group Meeting on Analog and Sensor Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(7) Summer School on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(8) Design to Test Symposium sponsored by VDEC

- (9) Technical Group Meeting on Low Power Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (10) VLSI Designer's Forum sponsored by VDEC
- (11) Technical Group Meeting on Processors sponsored by IEICE Integrated Circuits and Devices Technical Group
- (12) Technical Group Meeting on Design Methodologies and Packaging sponsored by IEICE Integrated Circuits and Devices Technical Group
- (13) Technical Group Meeting on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (14) Technical Group Meeting for young researchers sponsored by IEICE Integrated Circuits and Devices Technical Group
- (15) Technical Group Meeting on Processors sponsored by IEICE Integrated Circuits and Devices Technical Group
- (16) Technical Group Meeting on Micro-wave Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (17) Technical Group Meeting on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

#### Awards

For membership promotion and encouraging research activities of students, two awards are offered.

(1) IEEE SSCS Japan Chapter Academic Research Award

Two students who did an outstanding research in the VLSI field will be selected and awarded

(2) IEEE SSCS Japan Chapter VDEC Design Award

One students who did an outstanding research in the VLSI field will be selected and awarded

Benefits for chapter and SSCS (such as increasing SSCS membership) for above activities:

Through our activities, increasing SSCS membership is expected. For example, in the Asian Symposium on Advanced Image Sensors, Systems, and Related Circuit Technologies, IEEE members can get special discount, which will urge non-members to apply to a membership.

Budget details for above activities:

Domestic meetings:

(1) Report of ISSCC 2016

\$1,000 (conference room fee and rewards for conference supporters)

(2) Report of ASSCC 2016

\$500 (conference room fee and rewards for conference supporters)

(3) Meeting by Distinguished Lecturer

\$500 (conference room fee and rewards for conference supporters)

#### Awards

(1) IEEE SSCS Japan Chapter Academic Research Award

\$200

(2) IEEE SSCS Japan Chapter VDEC Design Award

\$100

Description of Previous Year Chapter Activities:

The following domestic meetings were held.

Sponsored by SSCS Japan Chapter

(1) Report of VLSI 2015

Japanese presenters in VLSI 2015 were invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 10 presenters were invited. Professor Asad Abidi of UCLA were invited to give a presentation.

(2) Report of ASSCC 2015 to be co-sponsored with SSCS Kansai Chapter

Japanese presenters in ASSCC 2015 were invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 10 presenters were invited.

(3) Meeting by Distinguished Lecturers

We invited a couple of Distinguished Lecturers to give presentations to VLSI engineers and researchers.

Co-sponsored by SSCS Japan Chapter and SSCS Kansai Chapter

- (4) Technical Group Meeting on Memory Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (5) LSI and System Workshop sponsored by IEICE Integrated Circuits and Devices Technical Group
- (6) Technical Group Meeting on Analog and Sensor Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (7) Summer School on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (8) Design to Test Symposium sponsored by VDEC
- (9) Technical Group Meeting on Low Power Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (10) VLSI Designer's Forum sponsored by VDEC
- (11) Technical Group Meeting on Processors sponsored by IEICE Integrated Circuits and Devices Technical Group
- (12) Technical Group Meeting on Design Methodologies and Packaging sponsored by IEICE Integrated Circuits and Devices Technical Group
- (13) Technical Group Meeting on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (14) Technical Group Meeting for young researchers sponsored by IEICE Integrated Circuits and Devices Technical Group
- (15) Technical Group Meeting on Processors sponsored by IEICE Integrated Circuits and Devices Technical Group
- (16) Technical Group Meeting on Micro-wave Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group
- (17) Technical Group Meeting on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

#### Awards

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(1) IEEE SSCS Japan Chapter Academic Research Award

Two students who did an outstanding research in the VLSI field were selected and awarded

(2) IEEE SSCS Japan Chapter VDEC Design Award

One students who did an outstanding research in the VLSI field were selected and awarded

Report of utilization of SSCS funds for last year:

N/A

Report of support of student activities at local institutions:

Awards for students:

– IEEE SSCS Japan Chapter Academic Research Award

Two students who have promoted an outstanding research in the VLSI field were selected and awarded.

– IEEE SSCS Japan Chapter VDEC Design Award

One student who are promoting and give a presentation of an outstanding research in the VLSI field were selected and awarded.

Bank Name: Mizuho Bank

Bank Account Name: IEEE SSCS J-CHAPTER

Bank Account Number: 1119878

SWIFT or ABA:

Street Address: 1-6-1 Takezono, Tsukuba Mitsui Building 5F

City: Tsukuba

State: Ibaraki

Zip: 305-0032

Country: Japan

Special instructions (including intermediary bank, if applicable):

Concentration or Custody (if applicable):

Bank Account Name for Concentration or Custody (if applicable):  
Account Number (also known as HOP number):